

Fig. 1

## Semiconductor Quantum Cryptographic Device and Method Gilbert et al.

Fig. 2

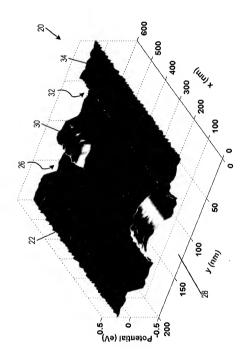
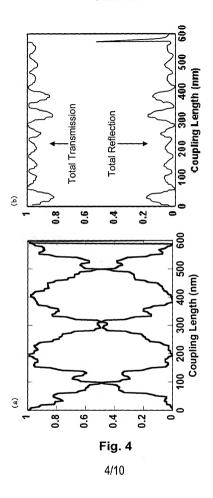
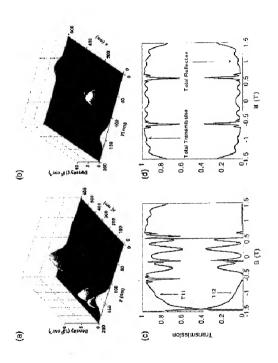


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**Fig. 5** 5/10

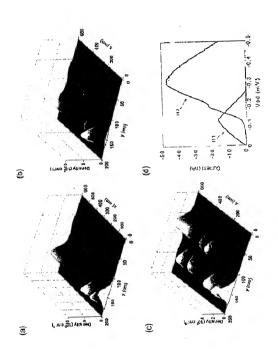


Fig. 6

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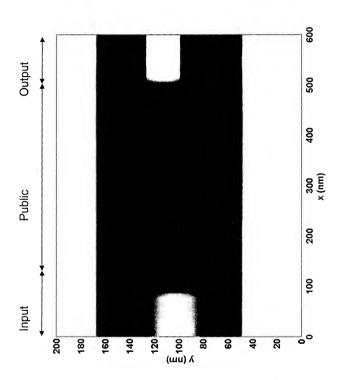
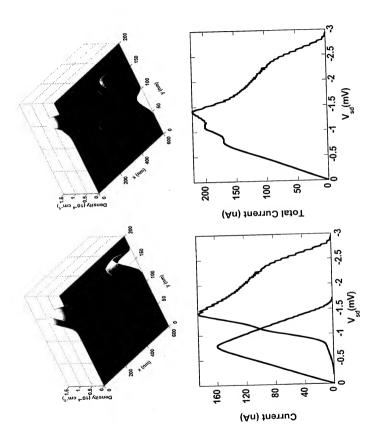


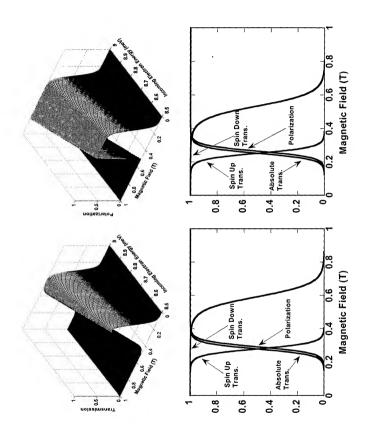
Fig. 7

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**Fig. 8** 8/10

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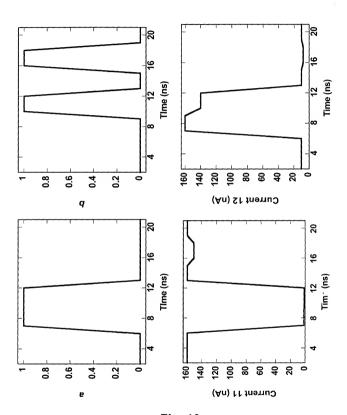


Fig. 10